



PS5000 Series User Manual (Slim Panel Type Atom Model) Corrections

Thank you for purchasing PS5000 Series.

PS5000 Series User Manual (Slim Panel Type Atom Model) contains the following errors. Please refer to the correction information given here.

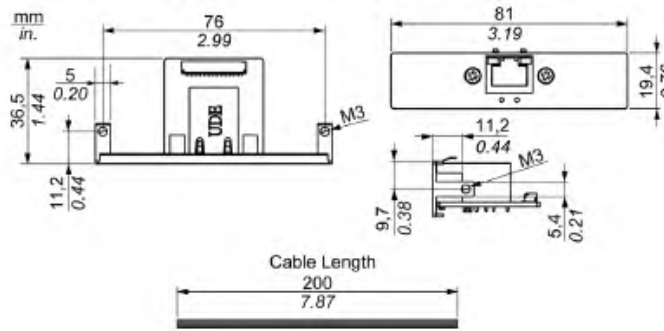
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42	<p>(Incorrect) No description as below.</p> <p style="text-align: center;">↓</p> <p>(Correct) HORM Win 10</p> <p>In HORM environment, a single hibernation file is used to restart the system repeatedly. To set a HORM environment, follow the steps below.</p> <p>Make sure that UWF is disabled (you can use ELM tool to disable UWF).</p> <p>Enable hibernation support: (you can use the Powercfg Command-Line options command-line tool to enable hibernation. The command is powercfg -h on (default is enable).</p> <p>Enable UWF by ELM tool. The system restarts.</p> <p>Open the software that customers want to use right after the system resumes from hibernation.</p> <p>Enable HORM by ELM tool. The system continues to use the HORM environment unless you disable HORM. You can use ELM tool to disable HORM.</p> <p>NOTE: Win 10 HORM function do not support UEFI BIOS, so PFXPL2B cannot use HORM function.</p>											
44	<p>(Incorrect)</p> <table border="1"> <thead> <tr> <th>Wire cross-section</th> <th>Maximum line length</th> </tr> </thead> <tbody> <tr> <td rowspan="2">2.5 mm² (AWG 14)</td> <td>30 m (98 ft)</td> </tr> <tr> <td>60 m (196 ft) round trip</td> </tr> </tbody> </table> <p style="text-align: center;">↓</p> <p>(Correct)</p> <table border="1"> <thead> <tr> <th>Wire cross-section</th> <th>Maximum line length</th> </tr> </thead> <tbody> <tr> <td>1.3 mm² (AWG 16)</td> <td>30 m (98 ft)</td> </tr> <tr> <td></td> <td>60 m (196 ft) round trip</td> </tr> </tbody> </table>	Wire cross-section	Maximum line length	2.5 mm ² (AWG 14)	30 m (98 ft)	60 m (196 ft) round trip	Wire cross-section	Maximum line length	1.3 mm ² (AWG 16)	30 m (98 ft)		60 m (196 ft) round trip
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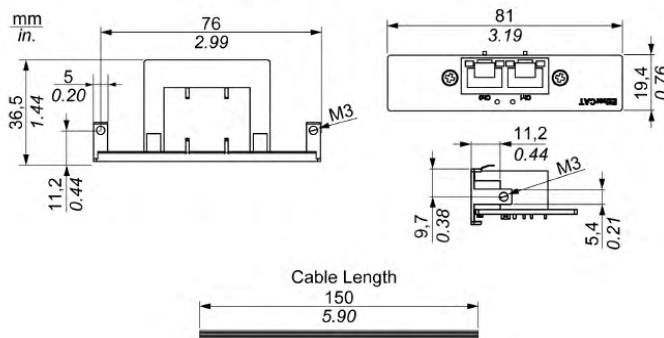
The figure shows the dimensions of the EtherCAT interface:



(Correct)



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(Incorrect)

Features	Values
General	
Bus type	mini PCIe card revision 1.2
Connectors	1 x RJ45 GbE half-/full-duplex
Power consumption	Max. 9 W at 3.3 V
Communication	
Speed	10/100/1000 base-TX, auto-negotiation
Support	9 K jumbo frames, hardware-based support for precise time synchronization over EtherCAT, wake-on-LAN



(Correct)

Features	Values
General	
Bus type	mini PCIe card revision 1.2
Connectors	2 x RJ45 GbE half-/full-duplex
Power consumption	Max. 9 W at 3.3 V
Communication	
Speed	10/100/1000 base-TX, auto-negotiation
Support	9 K jumbo frames, hardware-based support for precise time synchronization over EtherCAT, wake-on-LAN

120

(Incorrect)

No description as below.



(Correct)

Graphic Setting

Check that the BIOS Graphic of the Box is set to {IGFX}, as follows:

1. BIOS → Chipset → System Agent (SA) Configuration
2. Graphics configuration
3. Primary Display → IGFX
4. Save and exit BIOS